

**Notice of Allowability**

Application No.

10/080,734

Applicant(s)

YAMAMOTO, MASAKI

Examiner

Quynh H Nguyen

Art Unit

2642

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 2/22/02.
2. ☒ The allowed claim(s) is/are 1-32.
3. ☒ The drawings filed on 22 February 2002 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.


Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 2/22/02 & 7/29/02
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

  
**WILLIAM J. DEANE, JR.**  
PRIMARY EXAMINER

Quynh H. Nguyen  
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***Allowable Subject Matter***

1. Claims 1-32 are allowed.
2. The following is a statement of reasons for the indication of allowable subject matter:

Yamamoto et al. (U.S. Patent 6,226,503) teach a double tuned circuit comprising: a primary tuning a circuit; a secondary tuning circuit; a first coupling coil, a first switch diode and a second switch diode connected in series disposed between of the high-band tuning coil in the primary tuning circuit and the one end of the high-band tuning coil in the secondary tuning circuit; a second coupling coil, a first switching terminal and a second switching terminal, the anodes are connected to the first switching terminal; and a cathodes are connected to the second switching terminal.

Shinagawa et al. (U.S. Patent 4,247,953) teach a high frequency tuning input circuit for use in a television receiver receiving signals in two or more bands comprising: a first inductor connected at a first terminal thereof to a first terminal of a second inductor, a third inductor connected at a first terminal thereof to a second terminal of the second inductor, a capacitor connected between a second terminal of the first inductor and a second terminal of the third inductor, a first DC-blocking bypass capacitor and a variable capacitance diode connected in series between the first terminal of the first inductor and the second terminal of the third inductor. The bands to be received are

changed over by the on-off of the switching diode, and the resonant frequencies are varied by varying the capacitance value of the variable capacitance diode.

As to claims 1 and 15, the prior art of record fails to teach, or render obvious, alone or in combination, a high-frequency signal switching circuit comprising the claimed means and their components, relationships, and functionalities as specifically recited in claims 1 and 15.

Claims 2-14 and 16-27 are allowed because they depend on allowable claims 1 and 15, respectively.

As to claim 28, the prior art of record fails to teach, or render obvious, alone or in combination, a method decreasing distortion in high-frequency signal switching circuit, the method comprising the claimed means and their components, relationships, and functionalities as specifically recited in claim 28.

Claim 29-32 are allowed because they depend on allowable claim 28.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quynh H. Nguyen whose telephone number is 703-305-5451. The examiner can normally be reached on Monday - Thursday from 6:30 A.M. to 5:00 P.M.


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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ahmad Matar, can be reached on (703) 305-4731. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

qhn

Quynh H. Nguyen  
February 3, 2005



**WILLIAM J. BEANE, JR.**  
**PRIMARY EXAMINER**